

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Trafton et al. (TI-35750)

Serial No. ___/___/___

Group Art Unit:

Filed: Herewith

Examiner:

For: A Reconfigurable Topology for Switching and Charge Pump Negative Polarity
Voltage Regulators

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents

Washington, DC 20231

Dear Sir:

Applicants bring the references listed on the enclosed PTO/SB/08A to the attention of the Patent and Trademark Office relative to the subject application.

A copy of each reference is enclosed. Each reference is in the English language. As such, no additional statement of relevance is provided in this paper.

Consideration of this information in the prosecution of this application is respectfully requested.

Respectfully submitted,



Rodney M. Anderson
Registry No. 31,939
Attorney for Applicants

Anderson, Levine & Lintel, L.L.P.
14785 Preston Road, Suite 650
Dallas, Texas 75254
(972) 664-9554

Express Mail Label No. EU640981784US

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number

Filing Date

First Named Inventor

Trafton

Art Unit

Examiner Name

Attorney Docket Number

TI-35750

Sheet 1 of 1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		ON Semiconductor CS5111 Data Sheet, Rev. 9 (Semiconductor Components Industries, LLC, January 2001).	
		"Linear Regulators: Theory of Operation and Compensation", App.Nt. 1148 (Nat'l Semiconductor, 2000).	
		"Linear and Switching Voltage Regulator Fundamentals" (National Semiconductor).	
		"DC/DC Conversion without Inductors", APP 725 (Dallas Semiconductor, 2000).	

Examiner Signature	Date Considered
--------------------	-----------------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.